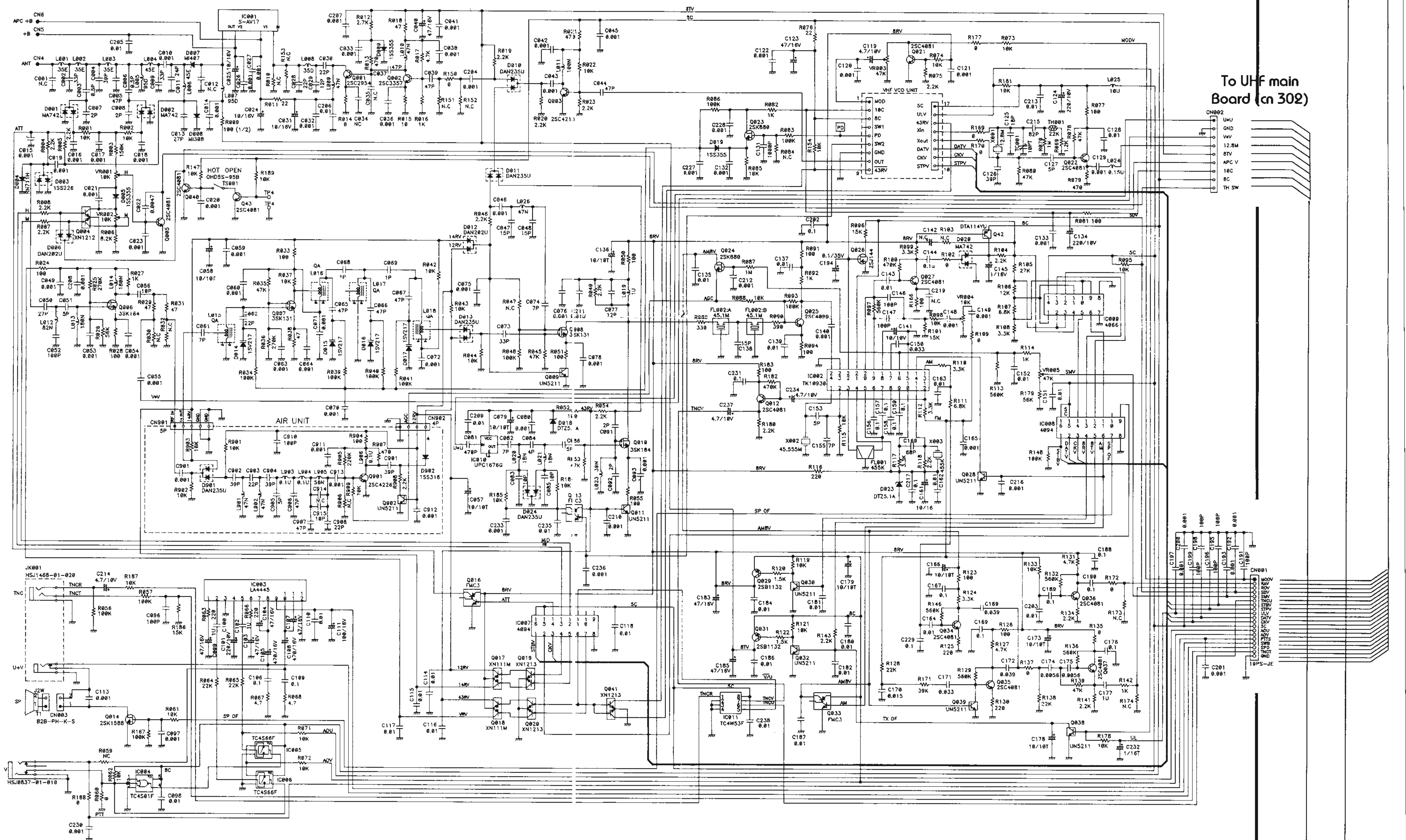


SCHEMATIC DIAGRAM DR-610T/E

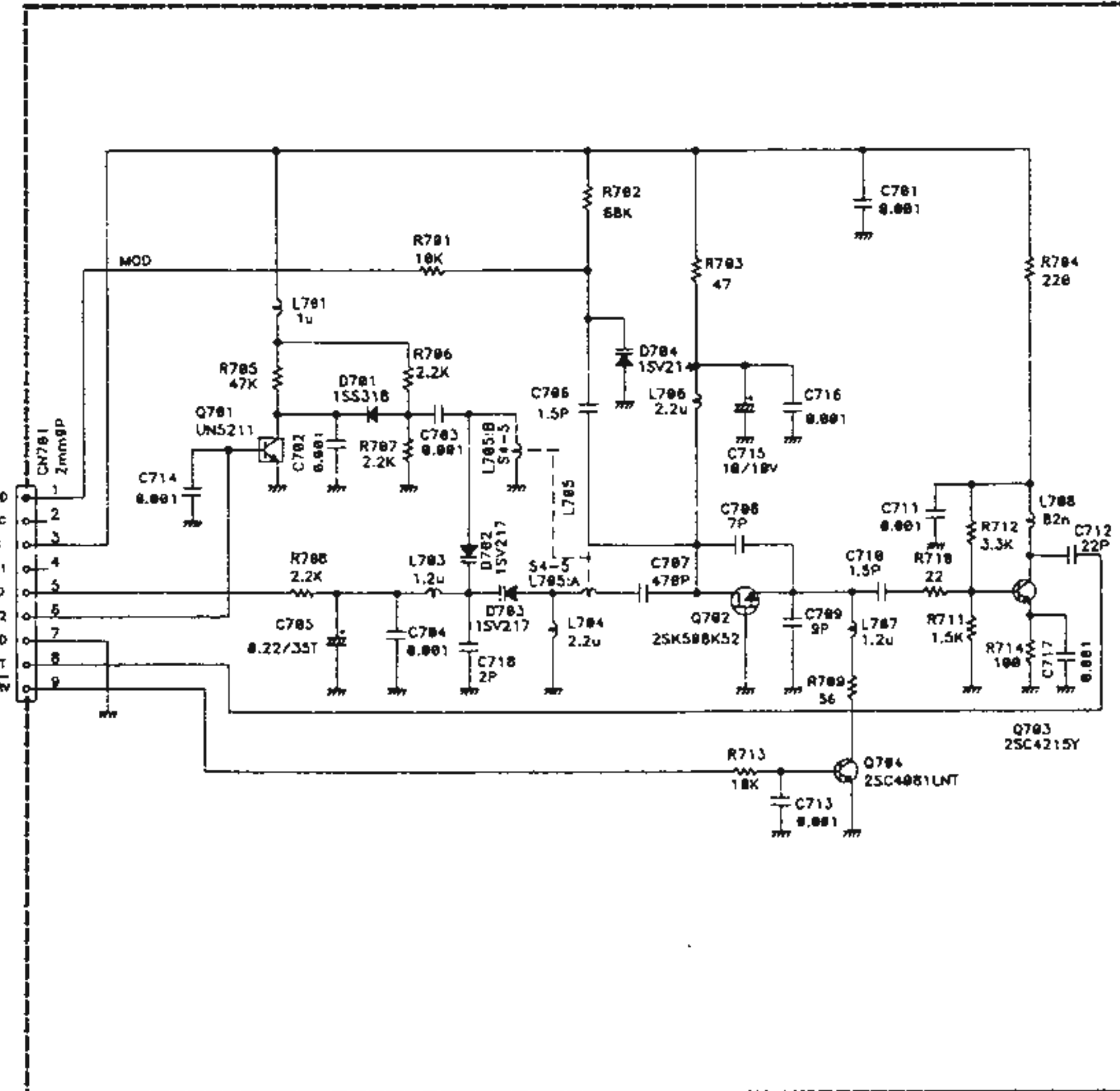
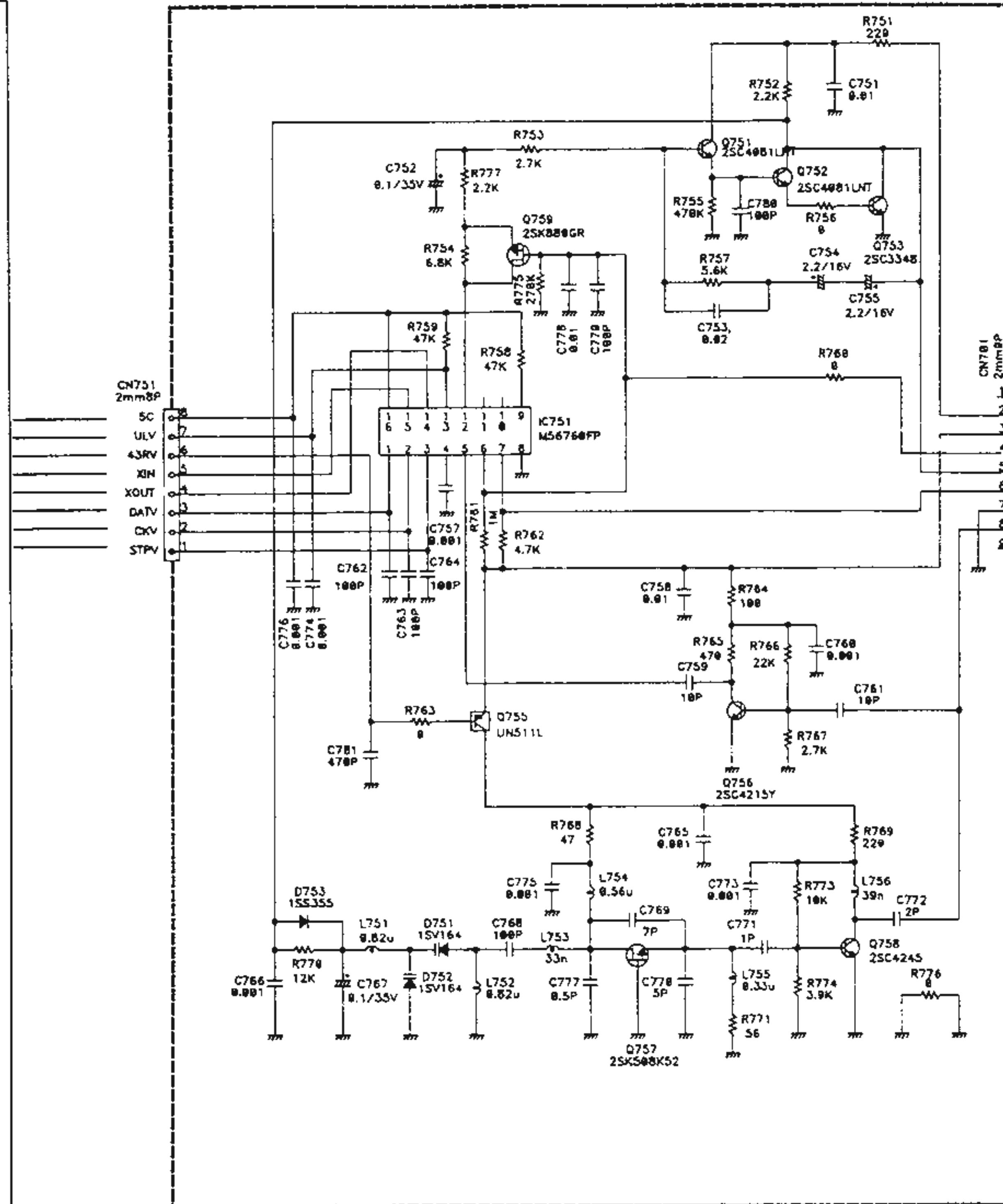
VHF MAIN UNIT



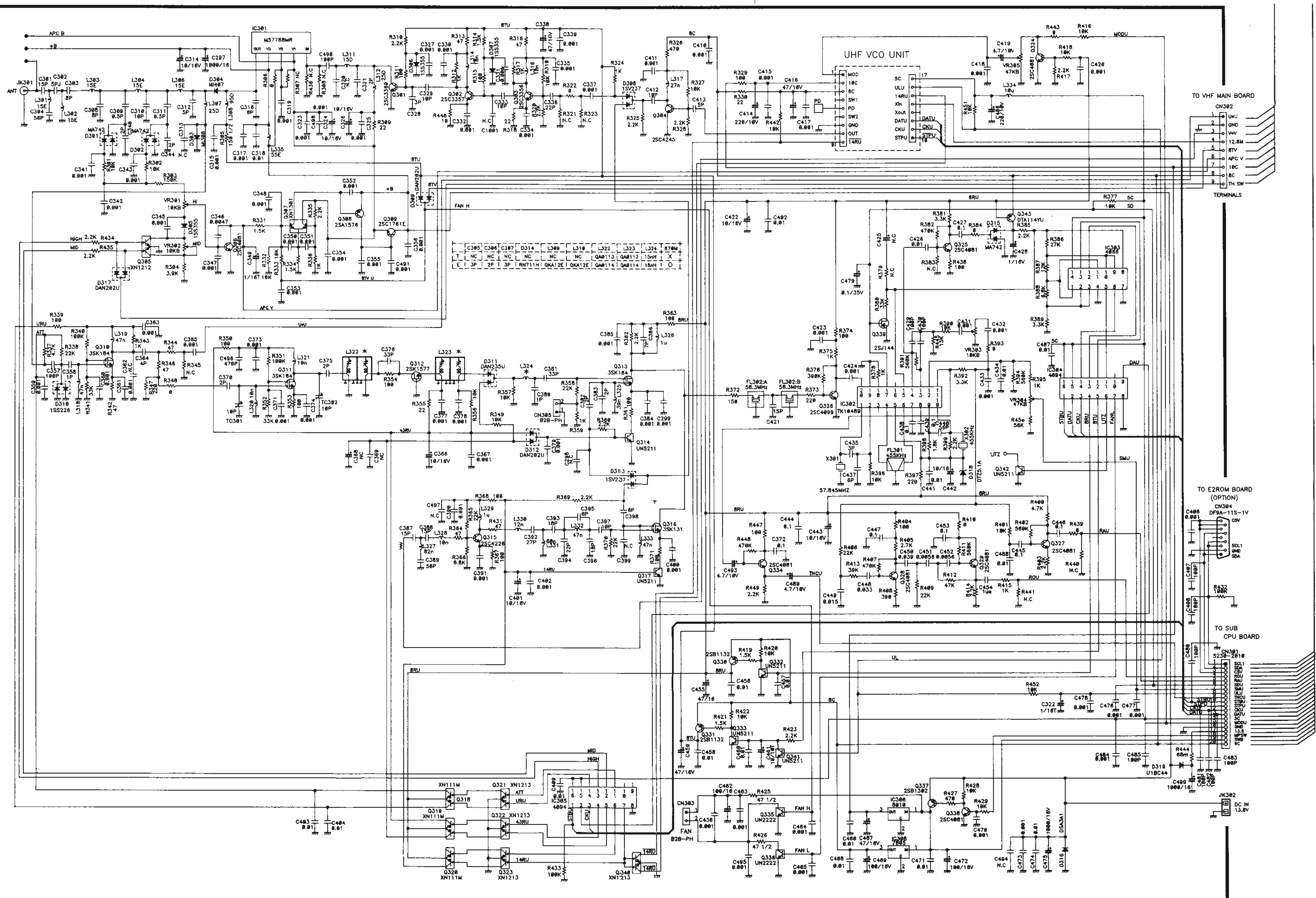
VHF PLL UNIT

VHF VCO UNIT

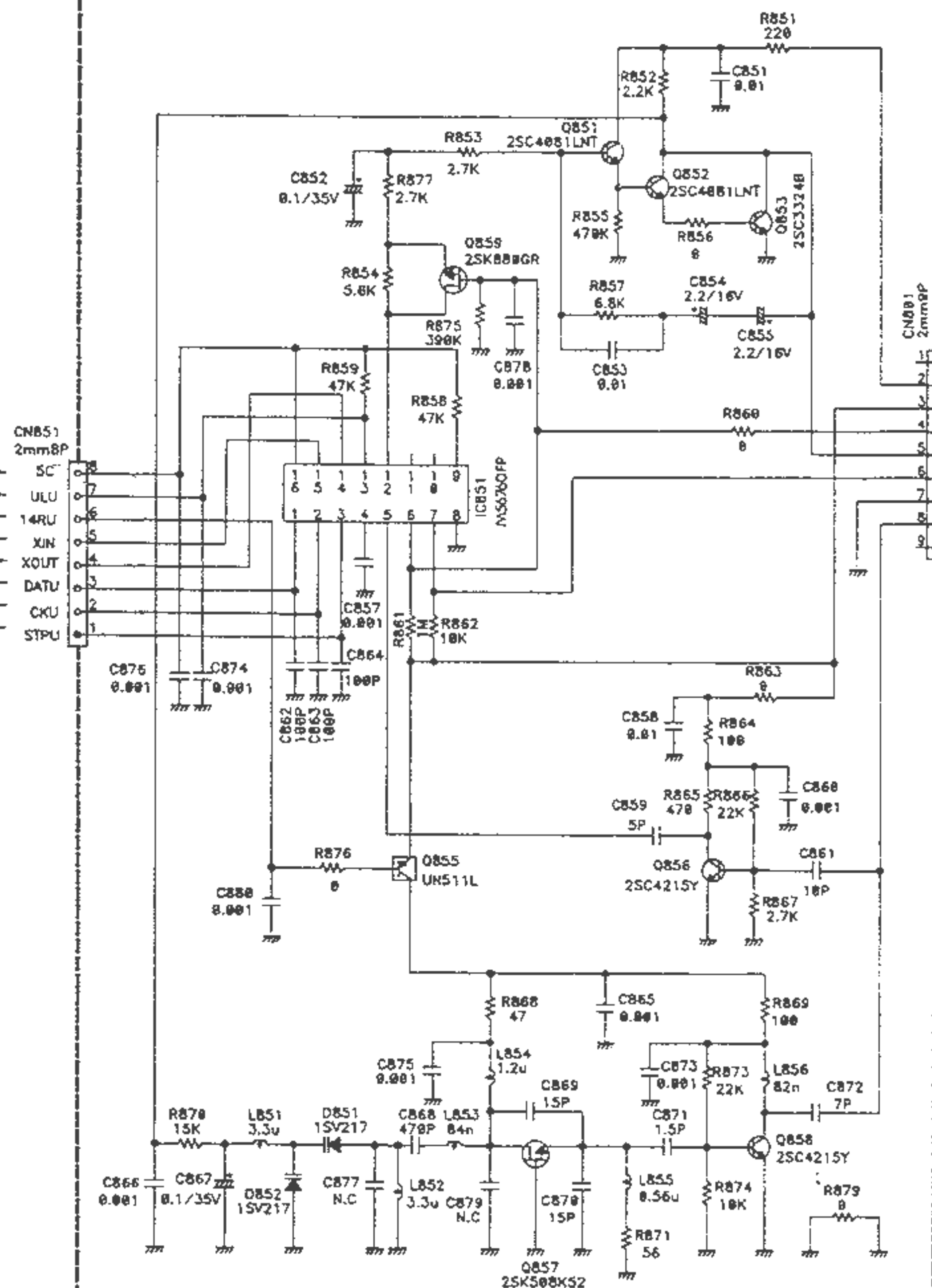
VHF MAIN UNIT



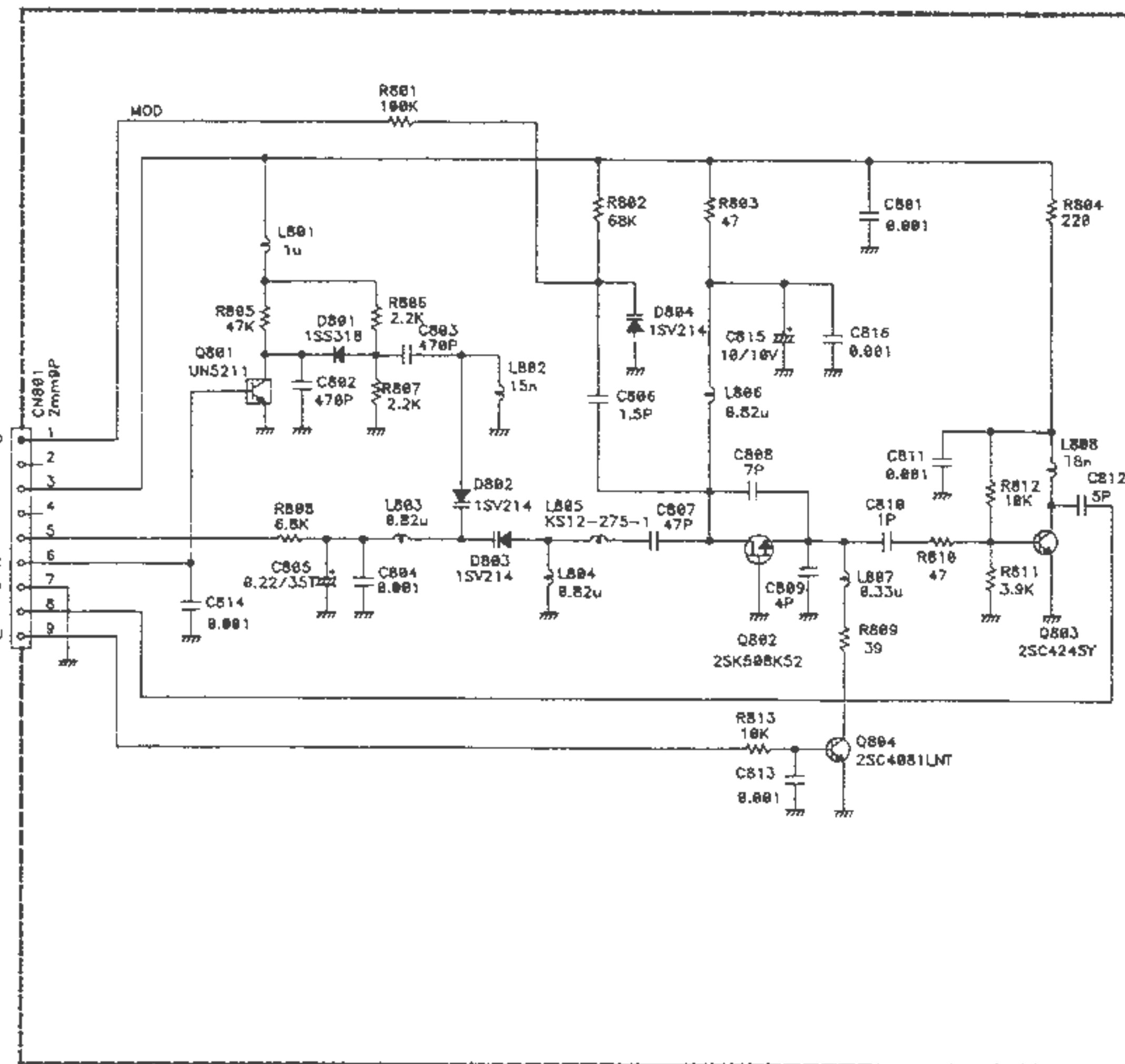
UHF MAIN UNIT



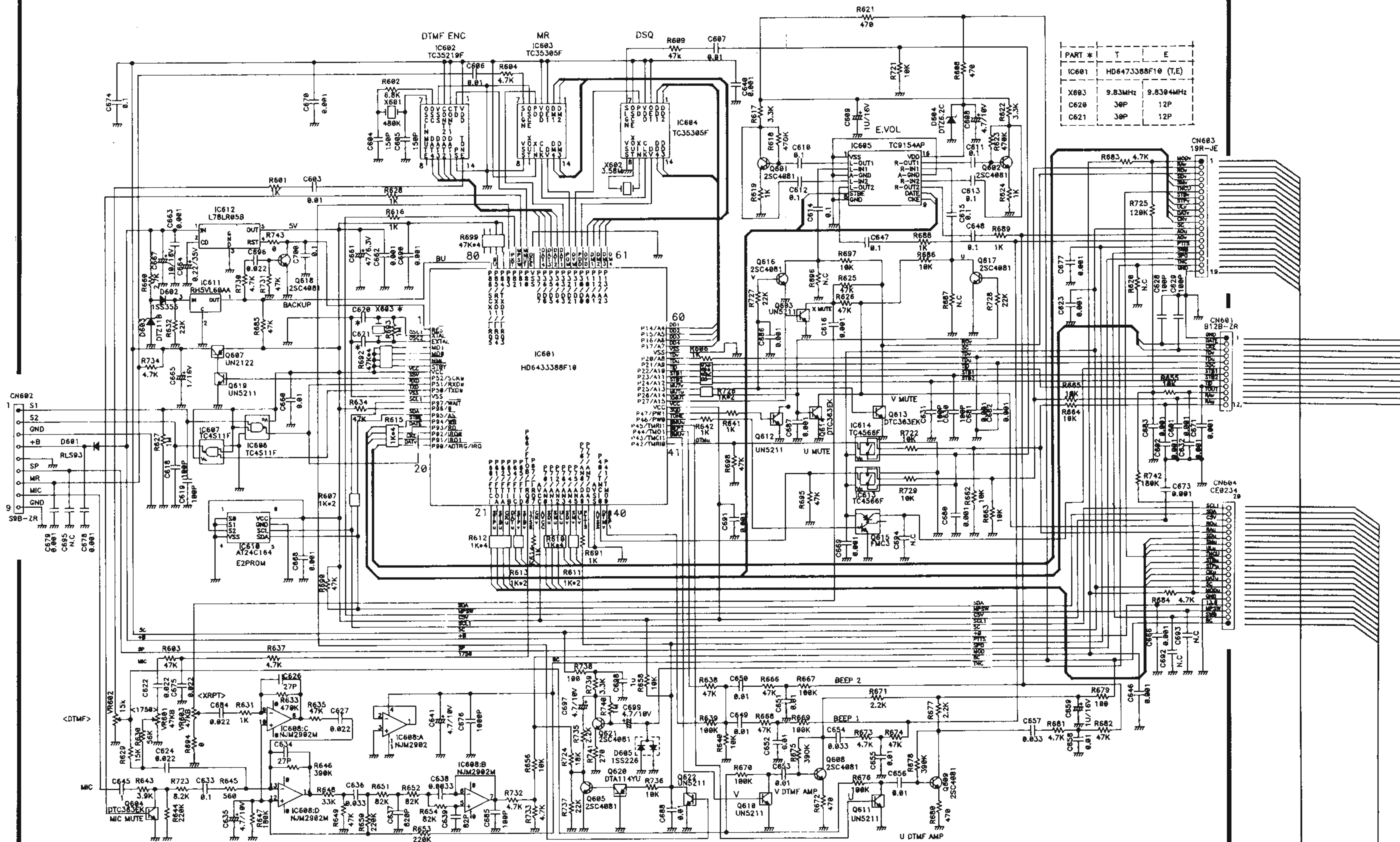
UHF PLL UNIT



UHF VCO UNIT



SUB CPU UNIT



PART #	T	E
IC601	HD6433388F10 (T,E)	
X603	9.83MHz	9.8304MHz
C620	30P	12P
C621	30P	12P

The schematic diagram illustrates the receiver section of a radio receiver. The central component is the IC981 (XA0280), which is a monolithic integrated circuit. The circuit includes a variable capacitor (VR981) and a variable capacitor (VR981) connected to the antenna input (CN981). The antenna input is connected to the IC981 through a network of resistors (R981, R982, R983, R984, R985, R986, R987, R988, R989, R990, R991, R992, R993, R994, R995) and capacitors (C981, C982, C983, C984, C985, C986, C987, C988, C989). The IC981 is also connected to a crystal (X981) and a variable capacitor (VR981). The output of the IC981 is connected to a speaker (Q981) and a variable capacitor (VR981). The circuit is powered by a 6.8V/7V battery (C982) and a 1.5V battery (C983). The IC981 is connected to ground through several resistors (R981, R982, R983, R984, R985, R986, R987, R988, R989, R990, R991, R992, R993, R994, R995) and capacitors (C981, C982, C983, C984, C985, C986, C987, C988, C989).